

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT		Atty. Docket: OKI 364	Application No.: Not yet assigned <u>10/630,869</u>
		Applicant: Kojo KAJITA	
		Filing Date: July 31, 2003	Group Art Unit: 2818 Not yet assigned

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Trans-Lation
P-D	AI	2001-257357	09/21/01	Japan			Abstract
	AJ						
	AK						
	AL						
	AM						

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

P-D	AN	Recessed-Channel Structure for Fabricating Ultrathin SOI MOSFET with Low Series Resistance, Mansun Chan, Fariborz Assaderaghi, Stephen A. Parke, Chenming Hu, Fellow, IEEE, and Ping K. Ko, Senior Member, IEEE IEEE Electron Device Letter, Vol. 15, No. 1, January 1994
Examiner	PHUC T. DANG	Date Considered <u>2/23/04</u>

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.